

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Inventor: Oswin Housty

Title: MEMORY CHECK ARCHITECTURE AND METHOD FOR A
MULTIPROCESSOR COMPUTER

App. No.: 10/763,009 Filed: 1/21/2004

Patent No.: n/a Issued: n/a

Examiner: (unknown) Group Art Unit:

Atty. Dkt. No.: 1458-TT5574

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

CHANGE OF CORRESPONDENCE ADDRESS

Dear Sir:

Please change the Correspondence Address for the above-identified application to:

34456
TOLER, LARSON & ABEL, LLP
5000 PLAZA ON THE LAKE, SUITE 265
AUSTIN, TX 78746

I am the Attorney of record.

Respectfully submitted,

Date

April 7, 2004

Paul J. Polansky
Paul J. Polansky, Reg. No. 33,992
Attorney for Applicant(s)
TOLER, LARSON & ABEL, L.L.P.